



PTO/SB/21 (08-03)

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		Application Number	10/685,352
		Filing Date	10/14/2003
		First Named Inventor	Valley
		Art Unit	
		Examiner Name	
Total Number of Pages in This Submission		Attorney Docket Number	HRL128

ENCLOSURES (Check all that apply)

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SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm or Individual name	CARY TOPE-MCKAY
Signature	
Date	10/23/2003

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Sheet

1

of

2

<i>Compl t if Kn wn</i>	
Application Number	10/685,352
Filing Date	10/14/2003
First Named Inventor	Valley
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Attorney Docket Number	HRL128

NON PATENT LITERATURE DOCUMENTS

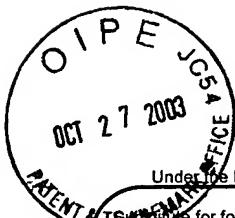
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	1	Y. Ahmed, A. Opal, "An efficient simulation method for oversampled delta-sigma modulators," Proc. of the 37th Midwest Symp. on Circuits and Systems, vol. 2, 1994, pp.1164-1167	
	2	A. Opal, "Sampled data simulation of linear and nonlinear circuits," IEEE Trans. CAD Integrated Circuits and Syst., vol. 15, no. 3, Mar. 1996, pp. 295-307	
	3	Y. Dong, et al, "Time-domain ther. noise sim. of switched capacitor circ. and delta-sigma modulators," IEEE Trans. CAD Int. Circ. and Syst., vol.19, no.4, Apr.2000, pp.473-481	
	4	R. Schreier and B. Zhang, "Delta-Sigma modulators employing continuous-time circuitry," IEEE Trans. Circuits and Syst. I, vol. 43, no. 4, Apr. 1996, pp. 324-332	
	5	J.A. Cherry, et al., "Approaches to simulating continuous-time delta-sigma modulators," Proc.of 1998 IEEE Int'l Sym. on Circ. and Sys., ISCAS'98, Vol. 1, 1998, pp. 587-590	
	6	D. Zhou, W. Cai, and Wu Zhang, "An adaptive wavelet method for nonlinear circuit simulation," IEEE Trans. Circuits and Syst. I, vol. 46, no. 8, Aug. 1999, pp. 931-938	
	7	A.P.S. Meliopoulos and C.-H. Lee, "An alternative method for transient analysis via wavelets," IEEE Trans. Power Delivery, vol. 15, no. 1, Jan. 2000, pp. 114-121	
	8	A.W. Galli, "Discussion of 'An alternative method for transient analysis via wavelets,'" Ibid, no. 4, Oct. 2000, p. 1326	
	9	A.P.S. Meliopoulos and C.-H. Lee, "Closure to discussion of 'An alternative method for transient analysis via wavelets,'" Ibid, no. 4, Oct. 2000, pp. 1326-1327	
	10	G. Raghavan, et al, "Arch., design, and test of contin.-time tunable interm.-freq. bandpass delta-sigma modulators," IEEE J. Solid-State Circ., vol.36, no.1, Jan.2001,pp.5-13	

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				Compl t if Known
				Application Number 10/685,352
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				First Named Inventor Valley
				Art Unit
				Examiner Name
Sheet 2		of 2		Attorney Docket Number HRL128

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	11	H.L. Resnikoff and R.O. Wells, Jr., "Wavelet Analysis," Springer, New York, 1998, pp. 236-265	
	12	H.L. Resnikoff and R.O. Wells, Jr., "Wavelet Analysis," Springer, New York, 1998, pp. 281 -340.	
	13	W.H. Press, et al., "Numerical Recipes in Fortran, the Art of Scientific Computing," 2nd Ed., Cambridge University Press, 1992, pp. 340-386	
	14	G. Beylkin, "On the representation of operators in bases of compactly supported wavelets," SIAM J. of Num. ana., vol.6, no.6, Dec1992, pp.1716-1740	
	15	M. Unser, et al., "Polynomial Splines and Wavelets – A Signal Processing Perspective," ed. By C.K. Chui, Academic Press, New York, 1992, pp. 91-122	

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